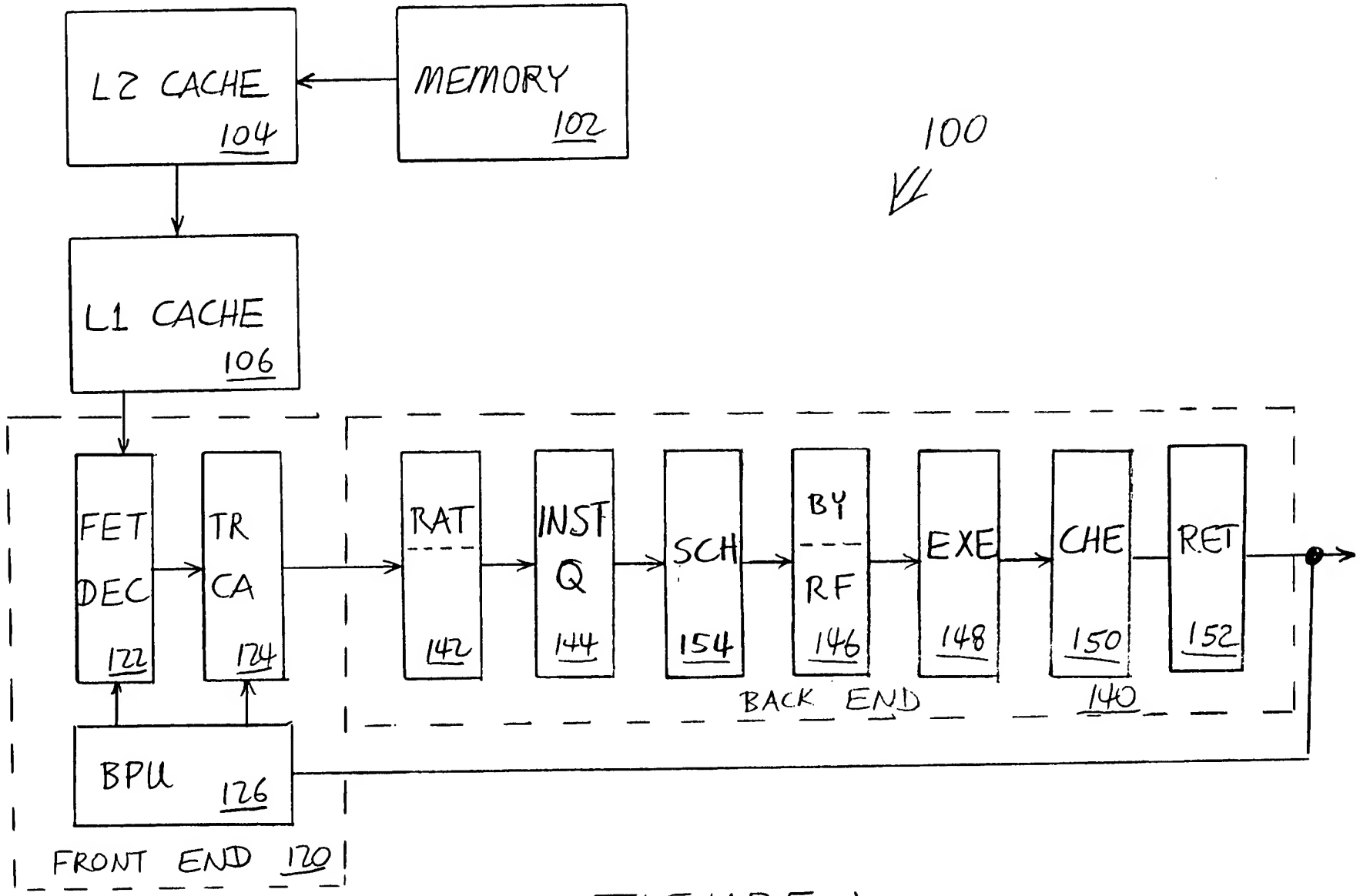
L2 CACHE
104
MEMORY
102
100
L1 CACHE
106
FET
DEC
122
TR
CA
124
RAT
142
INST
Q
144
SCH
154
BY
RF
146
EXE
148
CHE
150
RET
152
BACK END
140
BPU
126
FRONT END
120

FIGURE 1

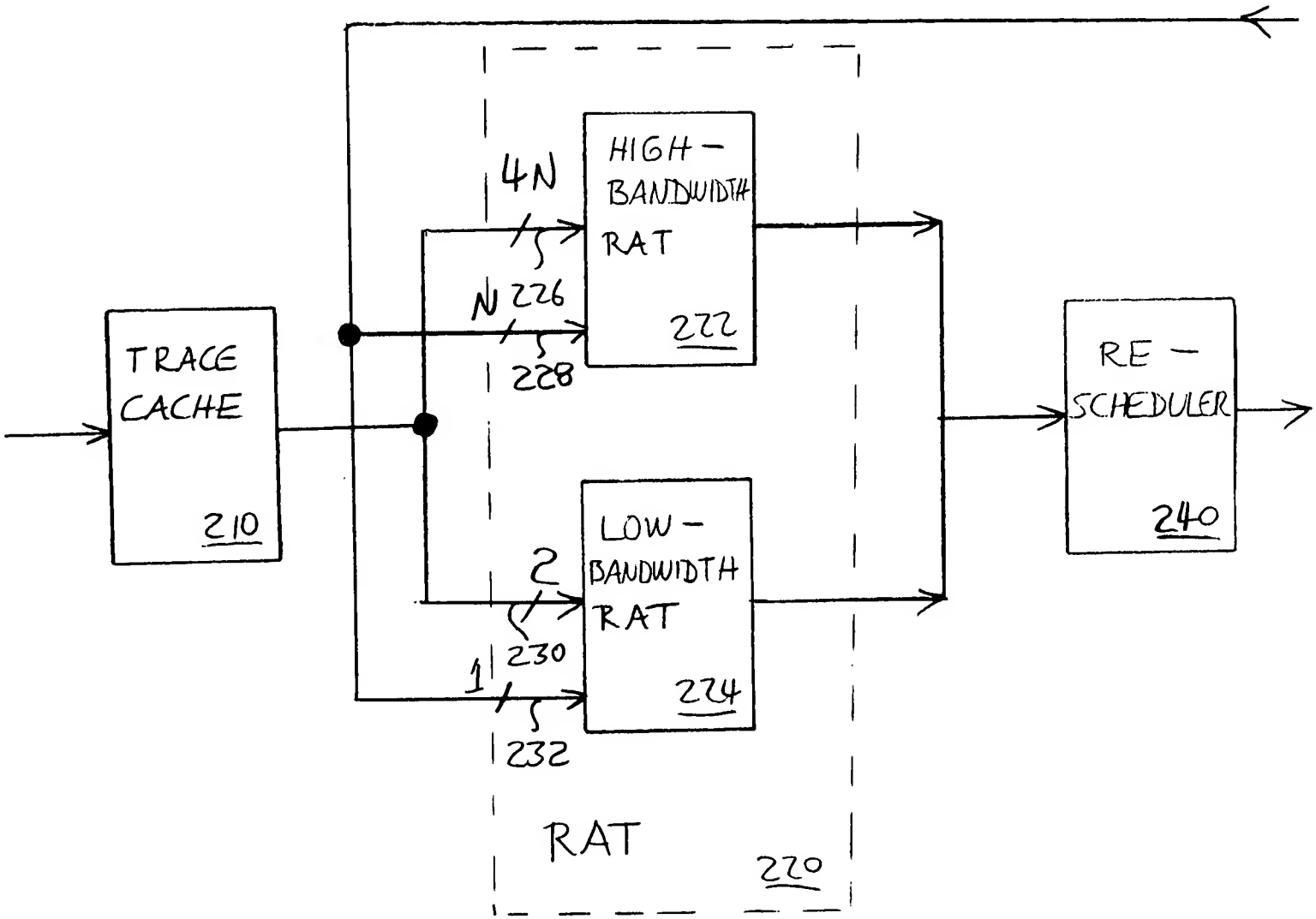
TRACE CACHE
210
4N
N
226
228
HIGH-BANDWIDTH RAT
222
2
230
1
232
LOW-BANDWIDTH RAT
224
RAT
220
RE-SCHEDULER
240

FIGURE 2

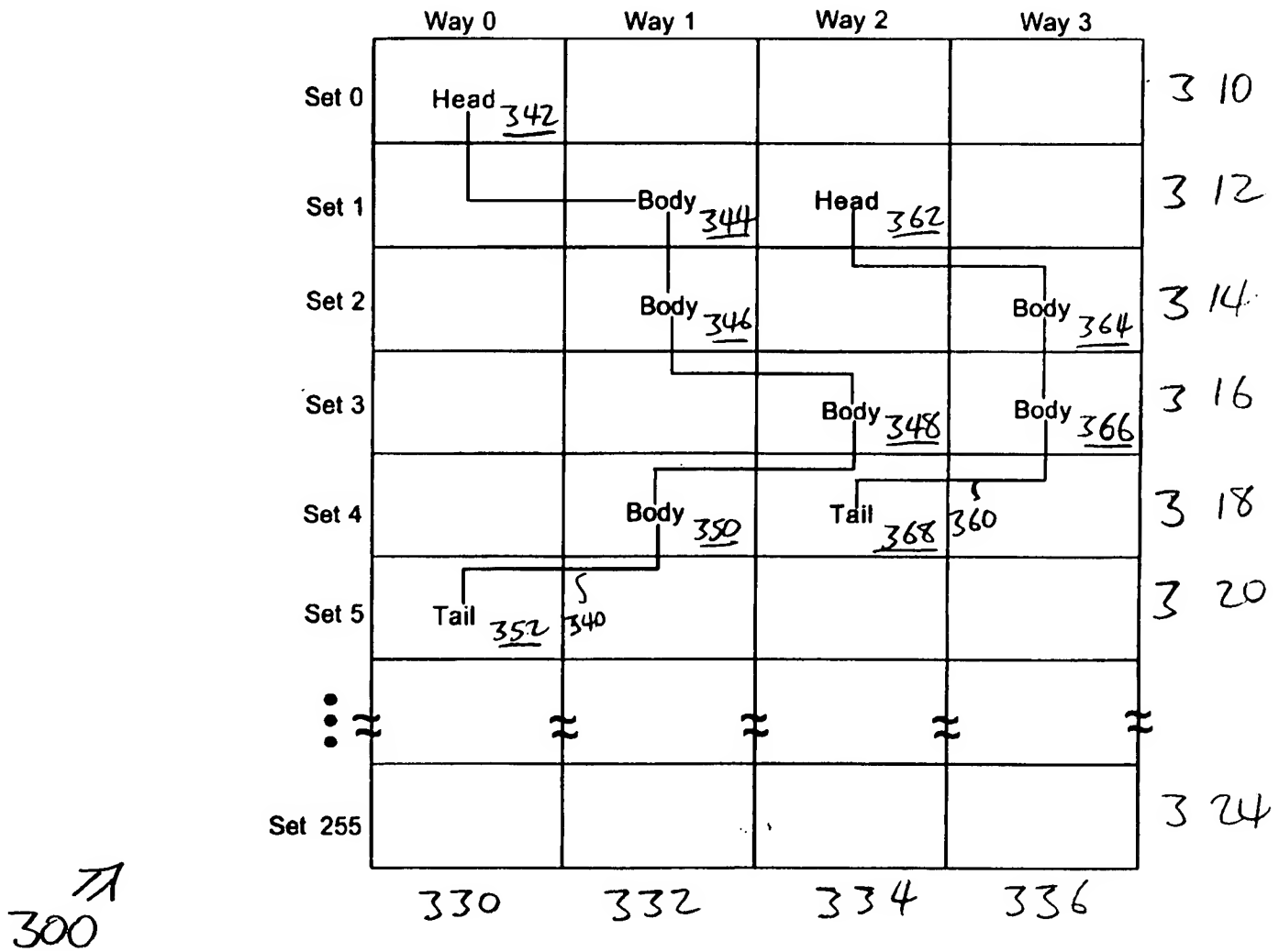
Way 0
Way 1
Way 2
Way 3
Set 0
Set 1
Set 2
Set 3
Set 4
Set 5
Set 255
Head 342
Body 344
Head 362
Body 346
Body 364
Body 348
Body 366
Body 350
Tail 368
360
Tail 352
340
310
312
314
316
318
320
324
330
332
334
336
300

FIGURE 3

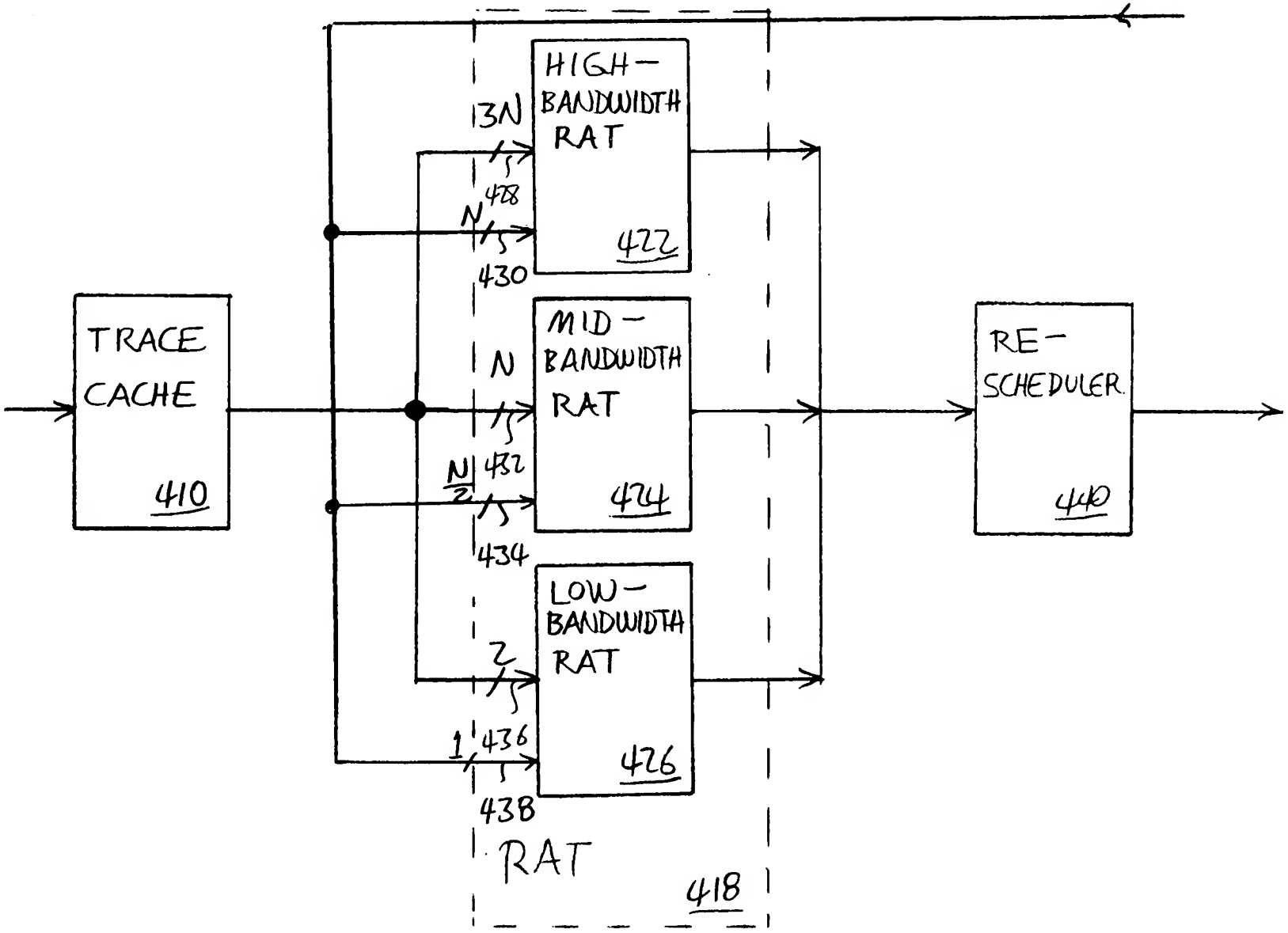
HIGH-
BANDWIDTH
RAT
422
3N
N
428
430
MID-
BANDWIDTH
RAT
424
N
N/2
432
434
LOW-
BANDWIDTH
RAT
426
2
1
436
438
TRACE
CACHE
410
RE-
SCHEDULER
440
RAT
418

FIGURE 4

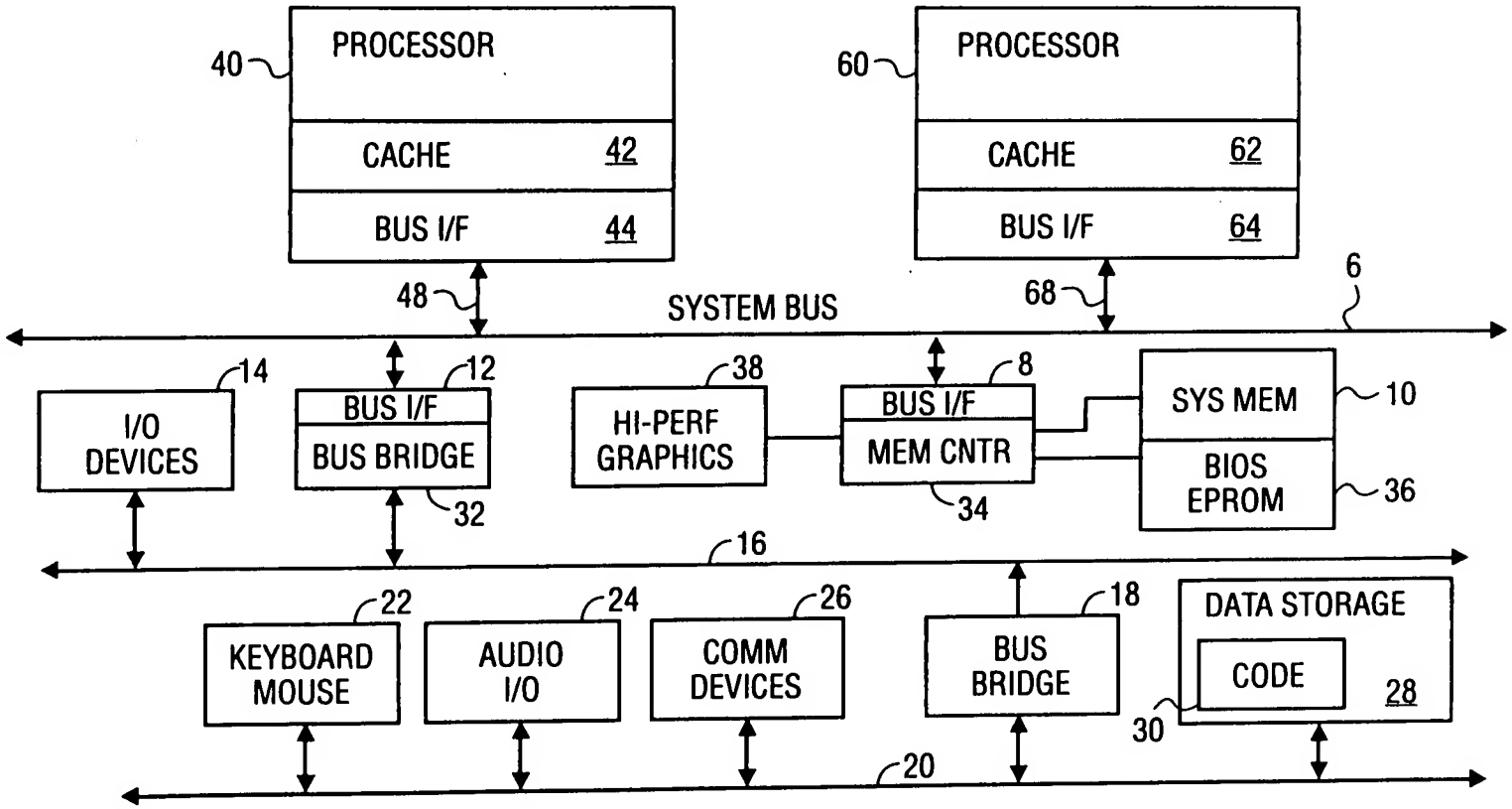
PROCESSOR
40
CACHE
42
BUS I/F
44
PROCESSOR
60
CACHE
62
BUS I/F
64
48
68
6
SYSTEM BUS
14
I/O DEVICES
12
BUS I/F
BUS BRIDGE
32
38
HI-PERF GRAPHICS
8
BUS I/F
MEM CNTR
34
SYS MEM
10
BIOS EPROM
36
16
22
KEYBOARD MOUSE
24
AUDIO I/O
26
COMM DEVICES
18
BUS BRIDGE
DATA STORAGE
CODE
28
30
20

FIGURE 5